

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :  
Yuichi HASHIMOTO et al. : **Attn: APPLICATION BRANCH**  
Serial No. NEW : **Attorney Docket No. 2003\_1596A**  
Filed November 18, 2003 :  
METHOD AND CIRCUIT FOR ERROR : **THE COMMISSIONER IS AUTHORIZED**  
CORRECTION, ERROR CORRECTION : **TO CHARGE ANY DEFICIENCY IN THE**  
ENCODING, DATA REPRODUCTION, OR : **FEEES FOR THIS PAPER TO DEPOSIT**  
DATA RECORDING : **ACCOUNT NO. 23-0975**

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**COVER LETTER ACCOMPANYING APPLICATION FILED WITHOUT EXECUTED**  
**DECLARATION UNDER 37 CFR 1.53(b) AND IN A LANGUAGE**  
**OTHER THAN ENGLISH UNDER 37 CFR 1.52(d)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed is a new patent application entitled "METHOD AND CIRCUIT FOR ERROR CORRECTION, ERROR CORRECTION ENCODING, DATA REPRODUCTION, OR DATA RECORDING". This application is submitted in the Japanese language under the provisions of 37 CFR 1.52(d).

Furthermore, the present application is submitted under the provisions of 37 CFR 1.53(b), and the application as filed does not include an executed declaration. However, accompanying the application is an unexecuted declaration listing the inventor information.

The application as filed further does not include an executed power of attorney, and accordingly, it is requested that communication initially be directed to the following firm, until an executed power of attorney and declaration are filed:

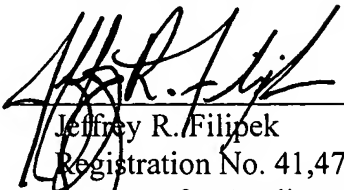
WENDEROTH, LIND & PONACK, L.L.P.  
2033 K Street, N.W., Suite 800  
Washington, D.C. 20006-1021

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The required filing fee of \$770.00 is enclosed.

Respectfully submitted,

Yuichi HASHIMOTO et al.

By   
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November 18, 2003

## DECLARATION AND POWER OF ATTORNEY FOR U.S. PATENT APPLICATION

(X) Original    () Supplemental    () Substitute    () PCT    () DESIGN

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Title: METHOD AND CIRCUIT FOR ERROR CORRECTION, ERROR CORRECTION ENCODING, DATA REPRODUCTION, OR DATA RECORDING

of which is described and claimed in:

() the attached specification, or

(X) the specification in application Serial No. \_\_\_\_\_, filed November 18, 2003, and with amendments through \_\_\_\_\_, or

() the specification in International Application No. \_\_\_\_\_, filed \_\_\_\_\_, and as amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the content of the above-identified specification, including the claims, as amended by any amendment(s) referred to above.

I acknowledge my duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim priority benefits under Title 35, United States Code, §119 (and §172 if this application is for a Design) of any application(s) for patent or inventor's certificate listed below and have also identified below any application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

COUNTRY	APPLICATION NO.	DATE OF FILING	PRIORITY CLAIMED
Japan	2002-333391	November 18, 2002	Yes
Japan	2003-169065	June 13, 2003	Yes

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION SERIAL NO.	U.S. FILING DATE	STATUS: PATENTED, PENDING, ABANDONED

And I hereby appoint Michael R. Davis, Reg. No. 25,134; Matthew M. Jacob, Reg. No. 25,154; Warren M. Cheek, Jr., Reg. No. 33,367; Nils Pedersen, Reg. No. 33,145; Charles R. Watts, Reg. No. 33,142; Michael S. Huppert, Reg. No. 40,268; and Jeffrey R. Filipek, Reg. No. 41,471, who together constitute the firm of WENDEROTH, LIND & PONACK, L.L.P., as well as any other attorneys and agents associated with Customer No. 000513, to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys and agents named herein to accept and follow instructions from AOYAMA & PARTNERS as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

Direct Correspondence to:  <div style="text-align: center; margin-top: 20px;"> <b>CUSTOMER NO.</b>  <b>000513</b> </div>		Direct Telephone Calls to:  <div style="text-align: center; margin-top: 20px;"> <b>WENDEROTH, LIND &amp; PONACK, L.L.P.</b>          2033 "K" Street, N.W., Suite 800          Washington, D.C. 20006-1021           Phone: (202) 721-8200          Fax: (202) 721-8250       </div>	
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<b>Post Office Address</b>	<b>ADDRESS</b>	<b>CITY</b>	<b>STATE OR COUNTRY</b>	<b>ZIP CODE</b>

I further declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

1st Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 Yuichi HASHIMOTO  
 2nd Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 Yuji TAKAGI  
 3rd Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 Makoto USUI  
 4th Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 Naohiro KIMURA  
 5th Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 Yoshikazu YAMAMOTO  
 6th Inventor \_\_\_\_\_ Date \_\_\_\_\_

The above application may be more particularly identified as follows:

U.S. Application Serial No. \_\_\_\_\_ Filing Date November 18, 2003

Applicant Reference Number 538202 Atty Docket No. 2003 1596A

Title of Invention METHOD AND CIRCUIT FOR ERROR CORRECTION, ERROR CORRECTION ENCODING, DATA REPRODUCTION, OR DATA RECORDING